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JUL 29 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/719,634
Filing Date November 21, 2003
Inventor Li et al.
Assignee Micron Technology, Inc.
Group Art Unit 2823
Examiner William M. Brewster
Attorney's Docket No. MI22-2441
Customer No. 021567
Title Processing Method of Forming MRAM Circuitry

RULE 312 RESPONSE

To: Mail Stop Issue Fee
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

VIA FACSIMILE

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Responsive to the Notice of Allowance dated June 23, 2005, Applicant
amends and remarks as follows:

AMENDMENTS

OK to Enter 9/0 65EP05